

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	2512	memory adj1 circuitry	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 08:51			0
2	BRS	L8	1125	1 and (capacitor or container or peripheral)	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 08:51			0
3	BRS	L15	1125	1 and 8	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 08:51			0
4	BRS	L22	792	15 and ((word adj1 lin\$3) or ell or trench or base or outline)	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 08:58			0
5	BRS	L29	792	22 and (read or write or memory or array or plurality)	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 08:59			0
6	BRS	L43	248	29 and (planar or thickness or angstroms or (digit adj1 lin\$2))	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 09:07			0
7	BRS	L50	242	43 and (outmost or top\$2 or node or storage or electro\$3)	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 10:41			0
8	BRS	L71	233	50 and (angstroms or thickness or elevat\$8 or top\$5 or surface)	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 10:49			0
9	BRS	L78	96	71 and (elevat\$7)	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD B	2002/06/24 10:49			0